

ABSTRACT OF THE DISCLOSURE

Sub  
Q2

5 A signal processing circuit outputs an output  
signal corresponding to a pulse width of an input pulse  
signal. This signal processing circuit comprises  
integrating means for integrating pulse widths of the  
input pulse signal for a predetermined period of time, and  
outputting means for outputting the output signal  
corresponding to the pulse widths integrated by the  
integrating means. Each of these pulse widths has one of  
10 positive and negative polarities.

100-1344-52208